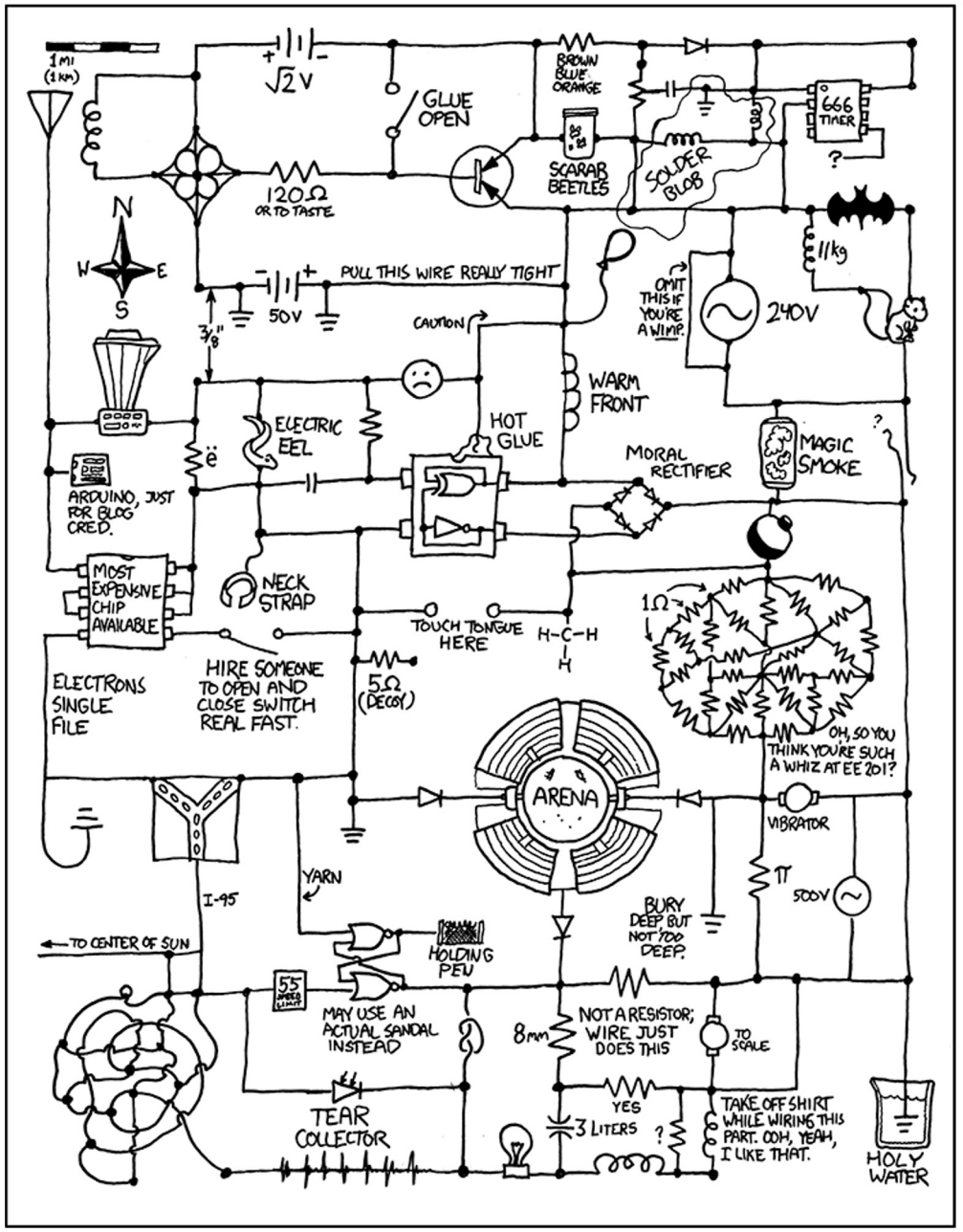


https://xkcd.com/730/

ENR-325/325L Principles of Digital Electronics and Laboratory

Xiang Li

Fall 2025

COE COLLEGE

Course Syllabus and Discussion

This introductory course provides a comprehensive foundation in Digital Electronics, starting with basic principles and progressing to digital design and system applications.

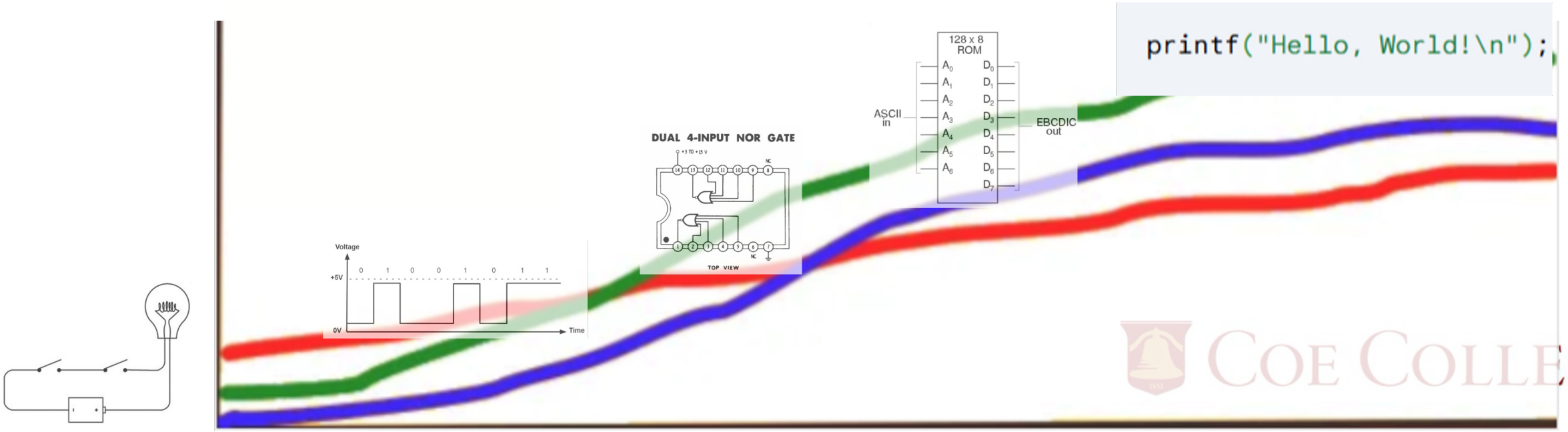
Through a combination of lectures, hands-on labs, and a final project, students will develop the skills to design, implement, and test basic digital systems and IoT prototypes. The course incorporates the use of digital logic simulation software and emphasizes the engineering design process and ethical considerations.

EP1: Digital and Information Systems

- The learning curve from EE to IC.
- Digital abstraction.
- Information theory and its application.

The learning curve from EE to IC

Presented learning curve from many EE/IC/CE courses.



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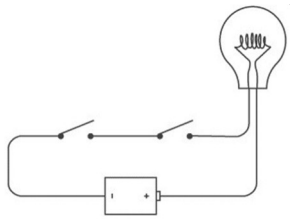
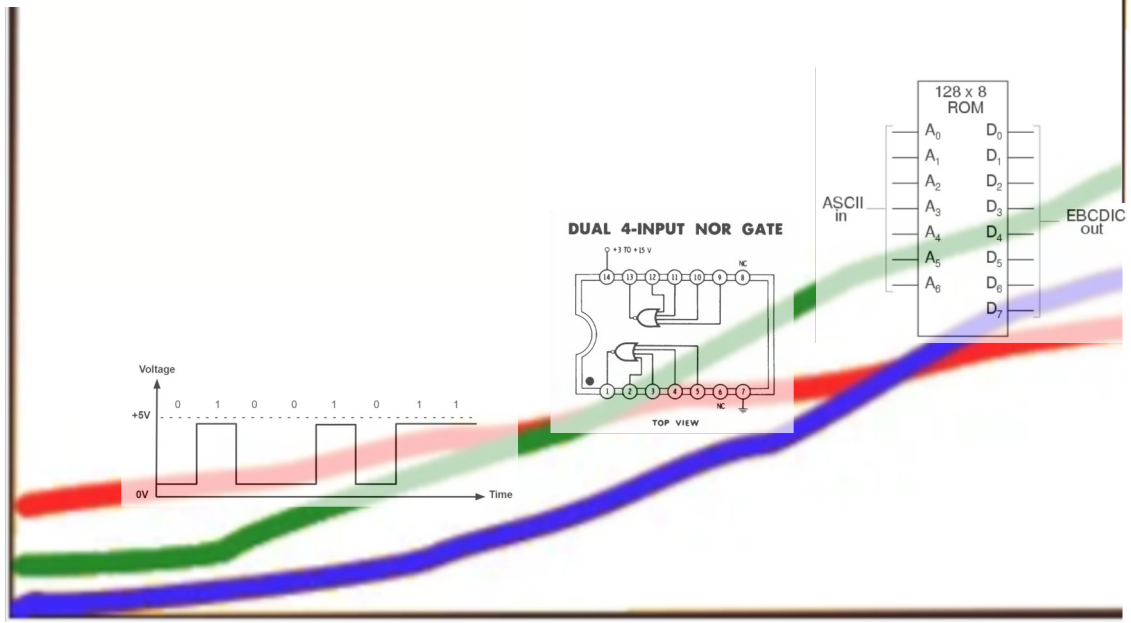
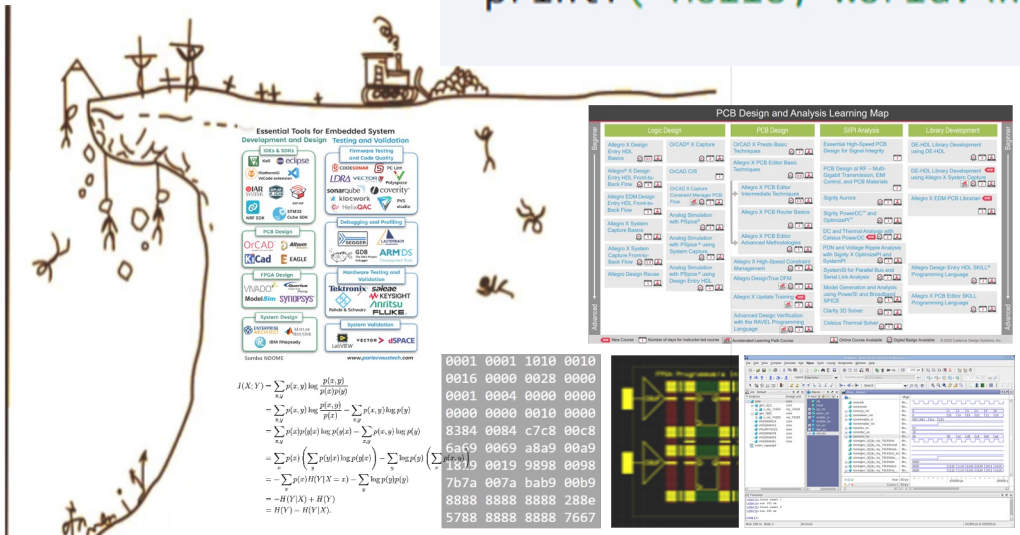
The learning curve from EE to IC

Actual learning curve to (part of the) industry.

```
printf("Hello, World!\n");
```







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The learning curve from EE to IC

The goal of this course:

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Why go digital? – the MATH/CS version (theory)

Symbolic logic

Computer arithmetic

Modern day CS

THE MATHEMATICAL ANALYSIS

OF LOGIC,

BEING AN ESSAY TOWARDS A CALCULUS
OF DEDUCTIVE REASONING.

BY GEORGE BOOLE.

Ἐπικοινωνοῦσι δὲ πᾶσαι αἱ ἐπιστῆμαι ἀλλήλαις κατὰ τὰ κοινά. Κοινὰ δὲ λέγω, οἷς χρῶνται ὡς ἐκ τούτων ἀποδεικνύντες· ἀλλ' οὐ περὶ ὧν δεικνύουσιν, οὐδὲ ὃ δεικνύουσι.

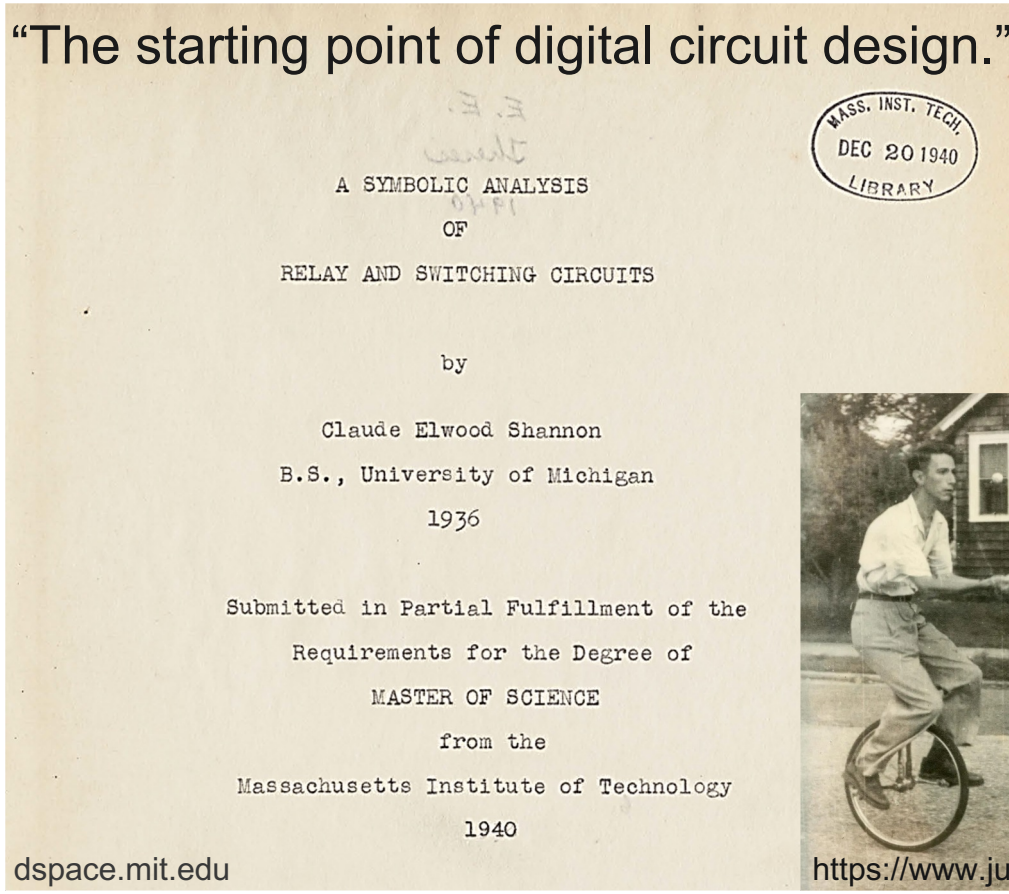
ARISTOTLE, *Anal. Post.*, lib. I. cap. XI.

CAMBRIDGE:
MACMILLAN, BARCLAY, & MACMILLAN;
LONDON: GEORGE BELL.

1847

gutenberg.org

"The starting point of digital circuit design."



dspace.mit.edu

https://www.juggle.org

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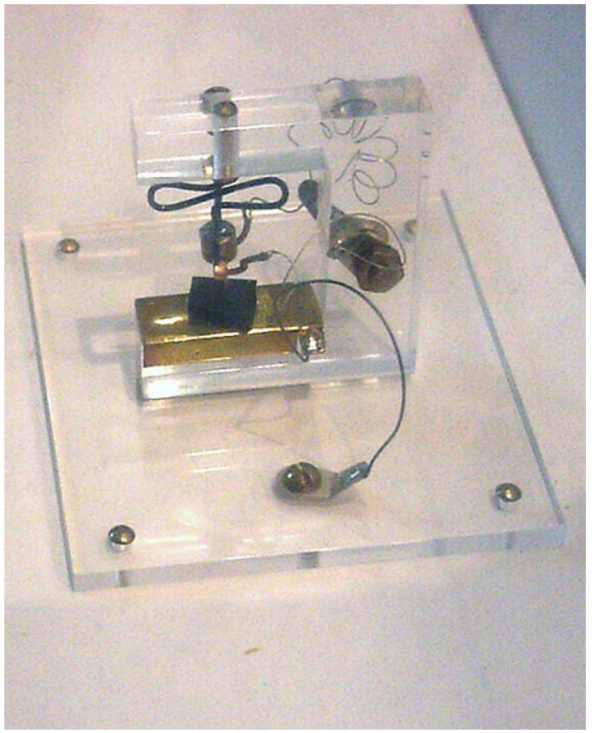
Shannon's juggling bot: https://www.youtube.com/watch?v=tXU3EPg2cgA

Why go digital? – the Computational/IC version (hardware)

Charles Babbage's Difference Engine, 1820s

Science Museum
London, UK

A replica of the world's first transistor, 1947

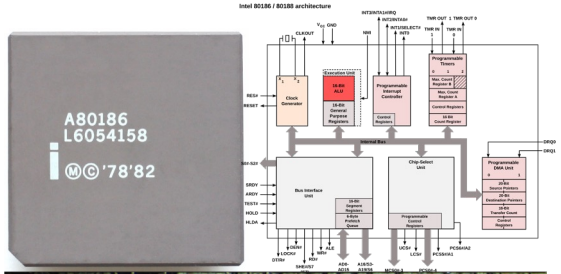


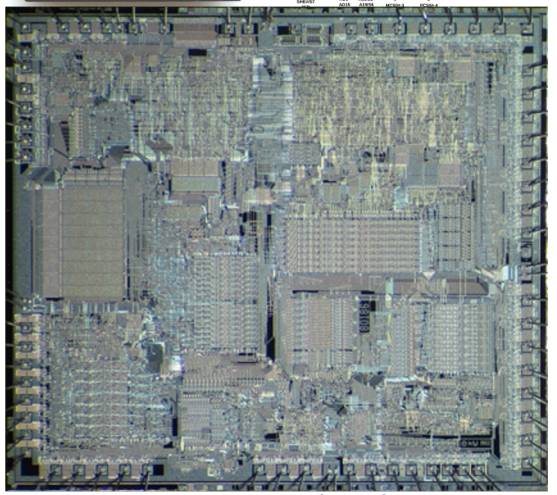
National Air and Space Museum
Washington, DC, USA

The IAS computer, 1952

www.computerhistory.org

Intel 80186, 1982





wikipedia.org/wiki/Intel_80186

Reference read: https://writings.stephenwolfram.com/2015/12/untangling-the-tale-of-ada-lovelace/

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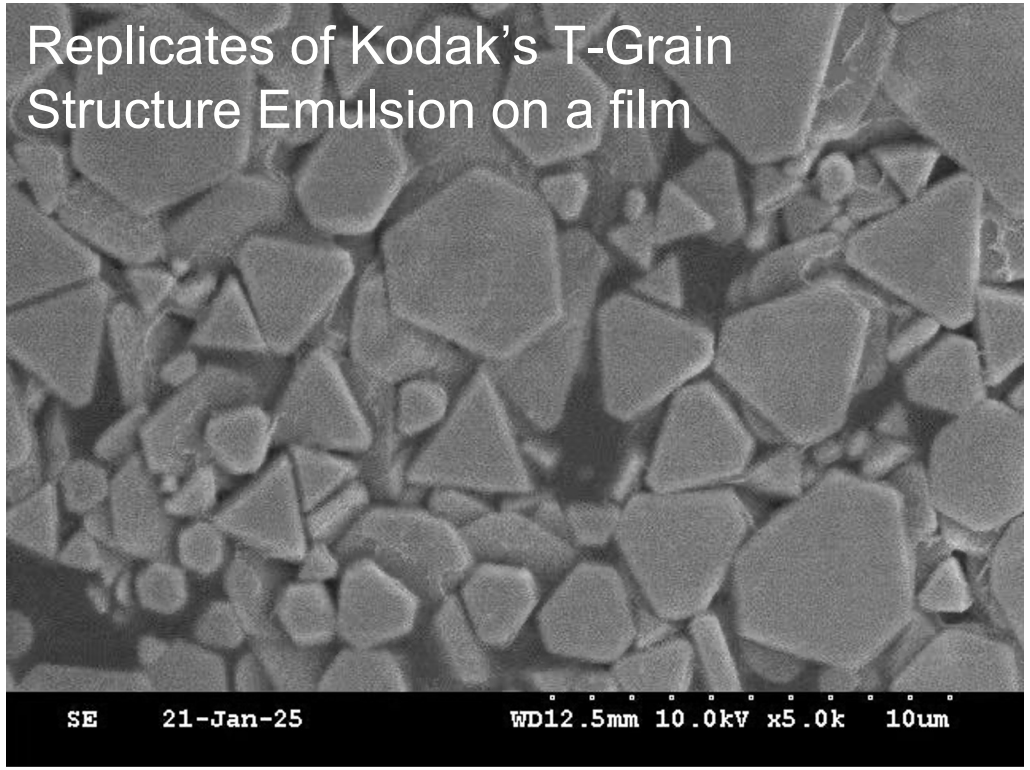
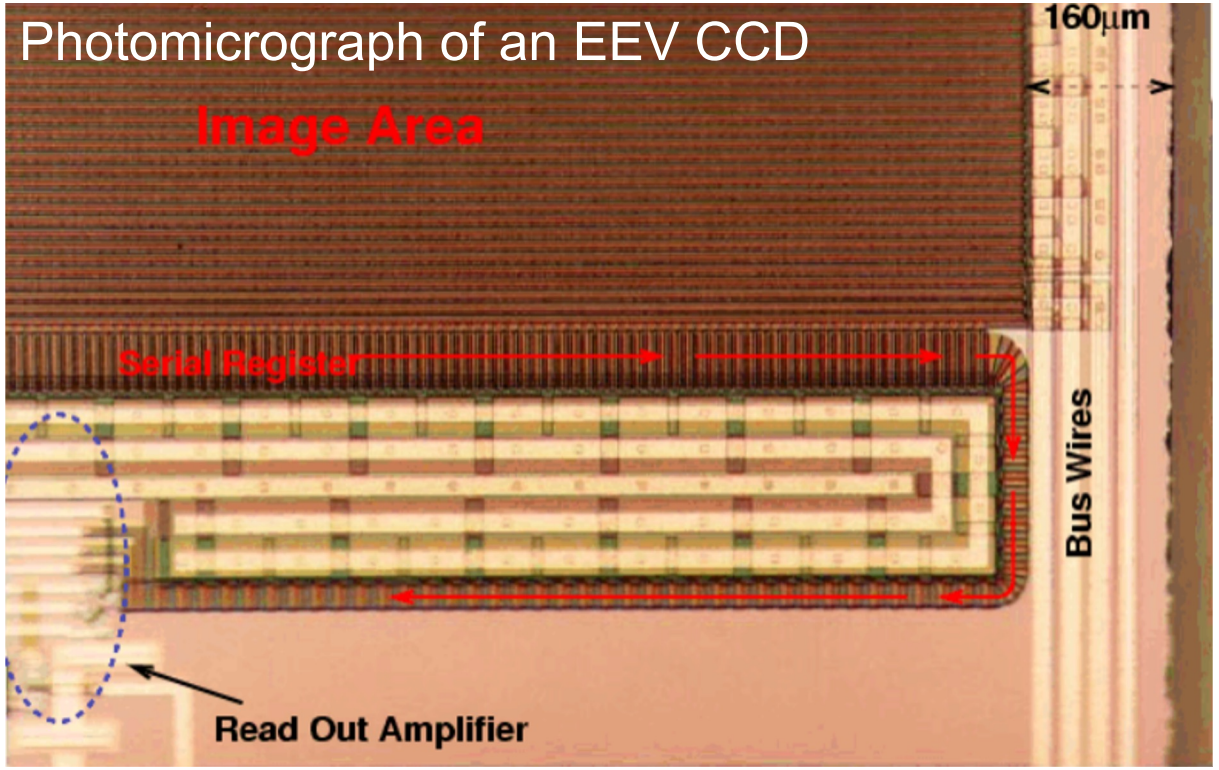
Why go digital? – the Phys/MicroE version (application)

Film camera vs CCD camera



lightlenslab.com



http://spiff.rit.edu/classes/phys445/lectures/ccd1/ccd1.html

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Why go digital? – the Phys/MicroE version (application, continued)

Film vs CCD by Liam Kix

https://youtu.be/YBT7PZrjKvw?si=Fgy_3RfV8aZ65oU-

OLLEGE

Why go digital? – the EE version

Because it's lazy and easy.

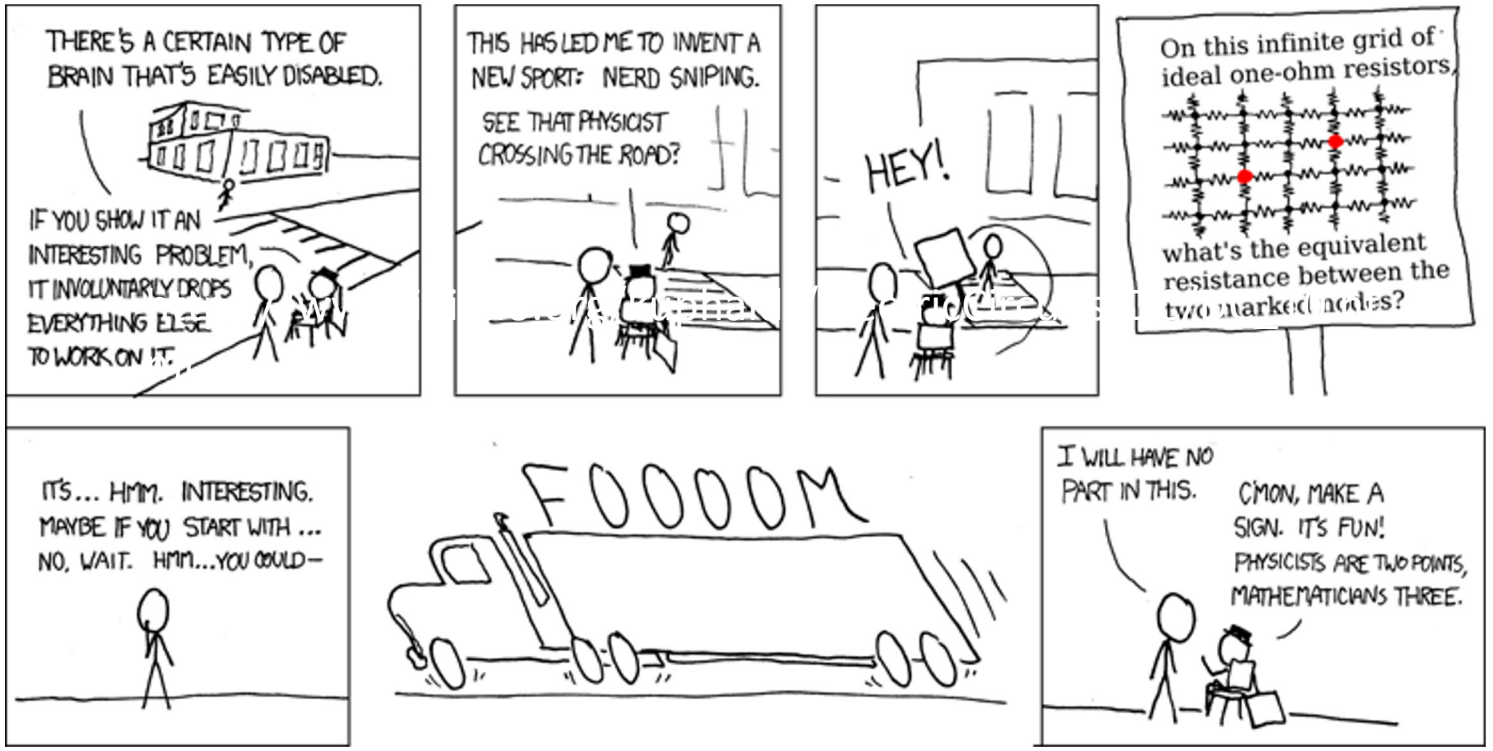
From analog circuits we learn:
LCA, KVL, KCL, node
For linear circuits: superposition, Thevenin, Norton

So instead of lumping matter, we will lump signal values, which will lead to the digital abstraction.

Class reflex: digital good

- Let’s share experience with boons we got from the digital age.

A quick detour to circuits analysis:



https://xkcd.com/356/

A place to refresh your memory:
https://www.ibiblio.org/kuphaldt/electricCircuits/DC/DC_10.html

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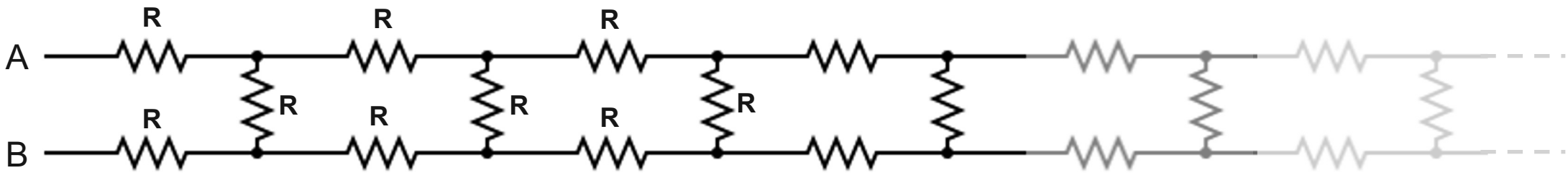
Let’s do an infinite resistor ladder puzzle



To get: equivalent resistor between point A and B, if the ladder go infinite.

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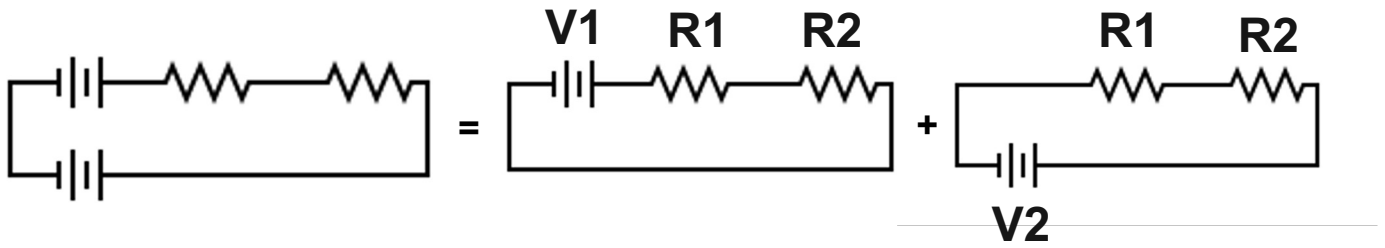
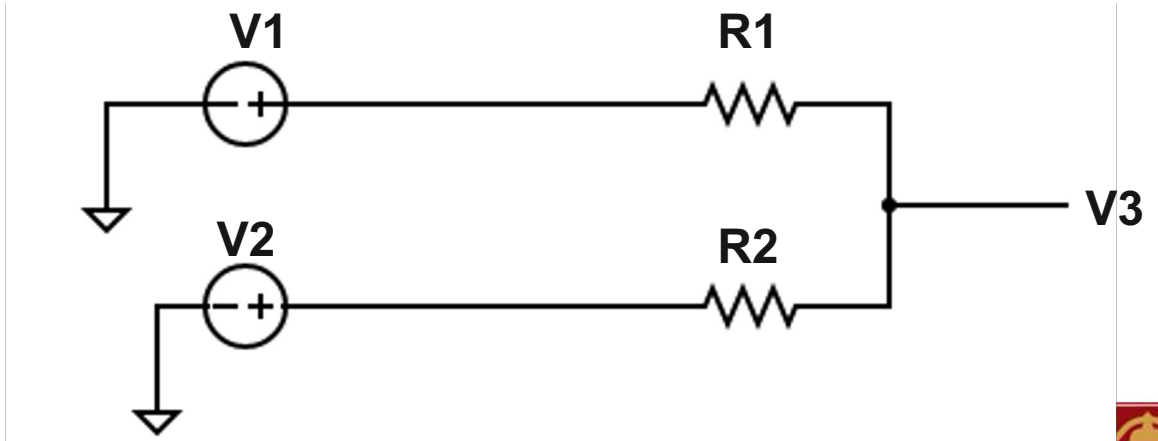
Why go digital? – the EE version

Your typical electric circuits about superposition is like:



The classical demo in telecommunications: noise resistance

Telecommunication: don't care about current but measure the voltage drop only:



© 2021 Circuit Diagram

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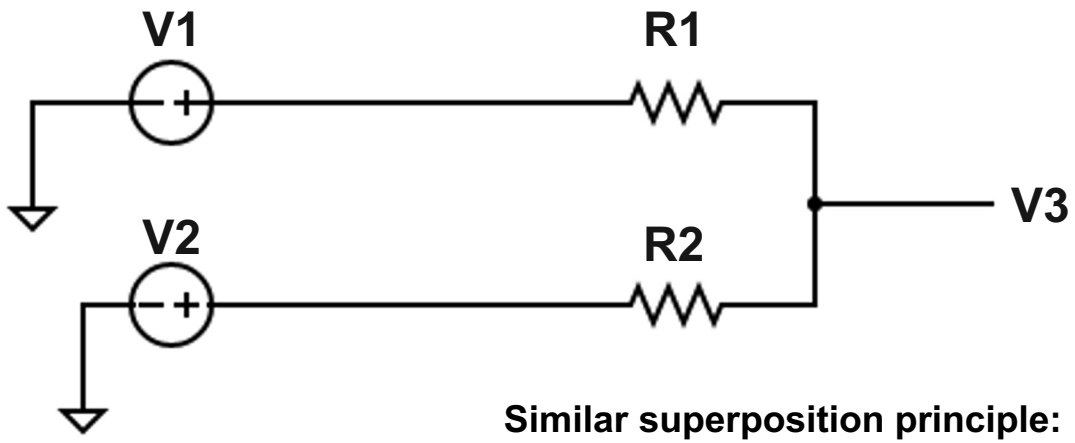
Why go digital? – the EE version

The classical demo in telecommunications: noise resistance



© 2021 Circuit Diagram

Similar superposition principle:

$$V3 = V1\frac{R2}{R1+R2} + V2\frac{R1}{R1+R2}$$

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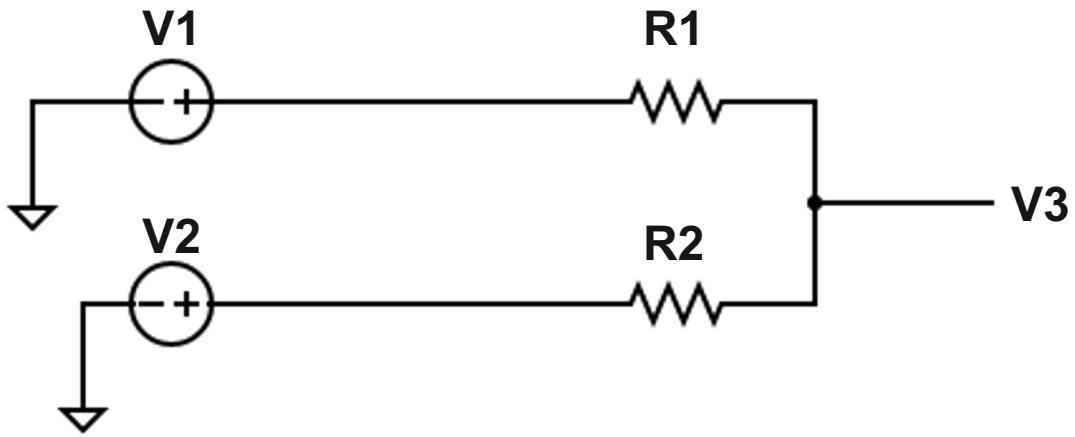
Why go digital? – the EE version

The classical demo in telecommunications: noise resistance



© 2021 Circuit Diagram

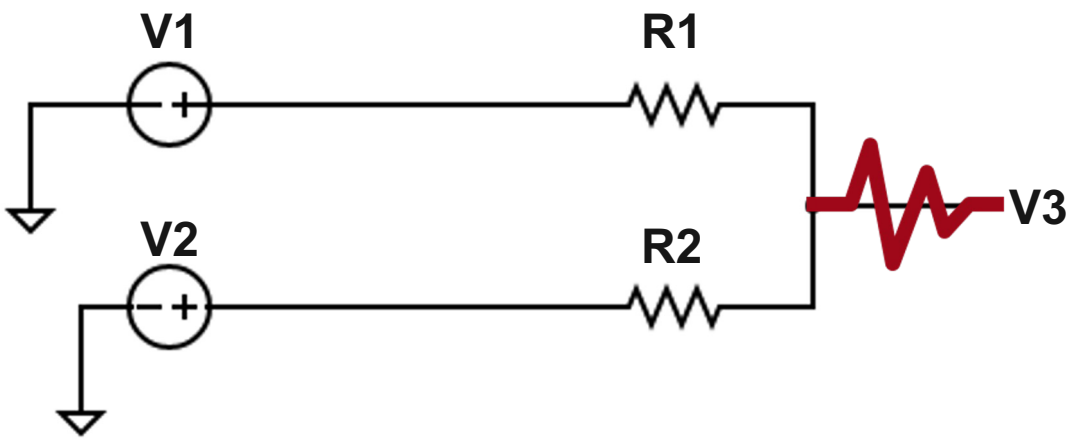
$$V3 = V1\frac{R2}{R1+R2} + V2\frac{R1}{R1+R2}$$

Two problems in telecommunications:

- Signals degrade over long distance.
- Noises introduced by… a lot of the things.

Why go digital? – the EE version

The classical demo in telecommunications: noise resistance



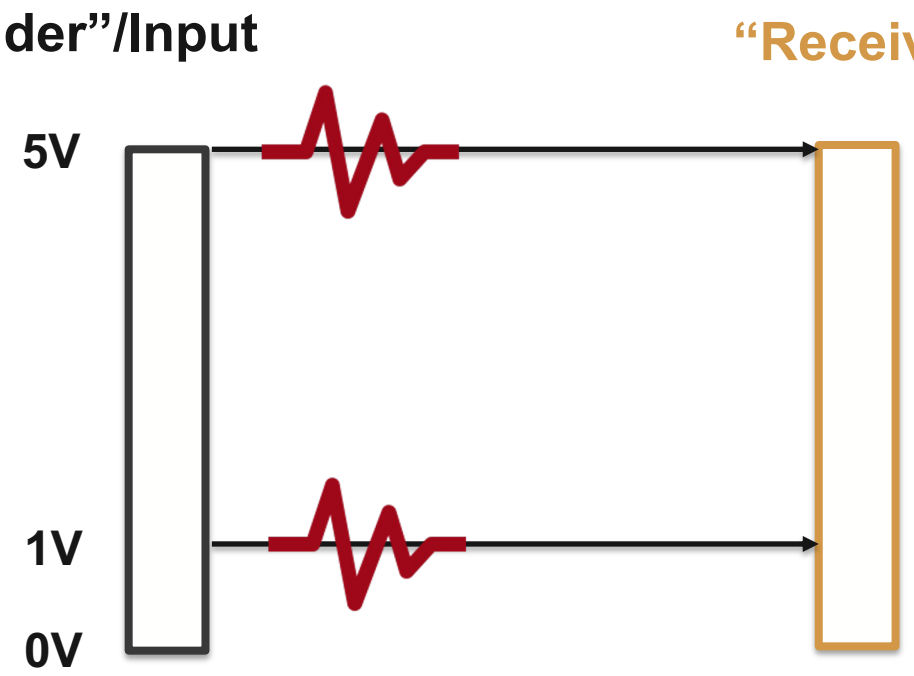
© 2021 Circuit Diagram

$$V3 = V1\frac{R2}{R1+R2} + V2\frac{R1}{R1+R2}$$

We can AMP signals along the way… but not forever.

Why go digital? – the EE version

The classical demo in telecommunications: the very foundation of the digital abstraction



“True”/logic high/binary 1

Issues?

“False”/logic low/binary 0

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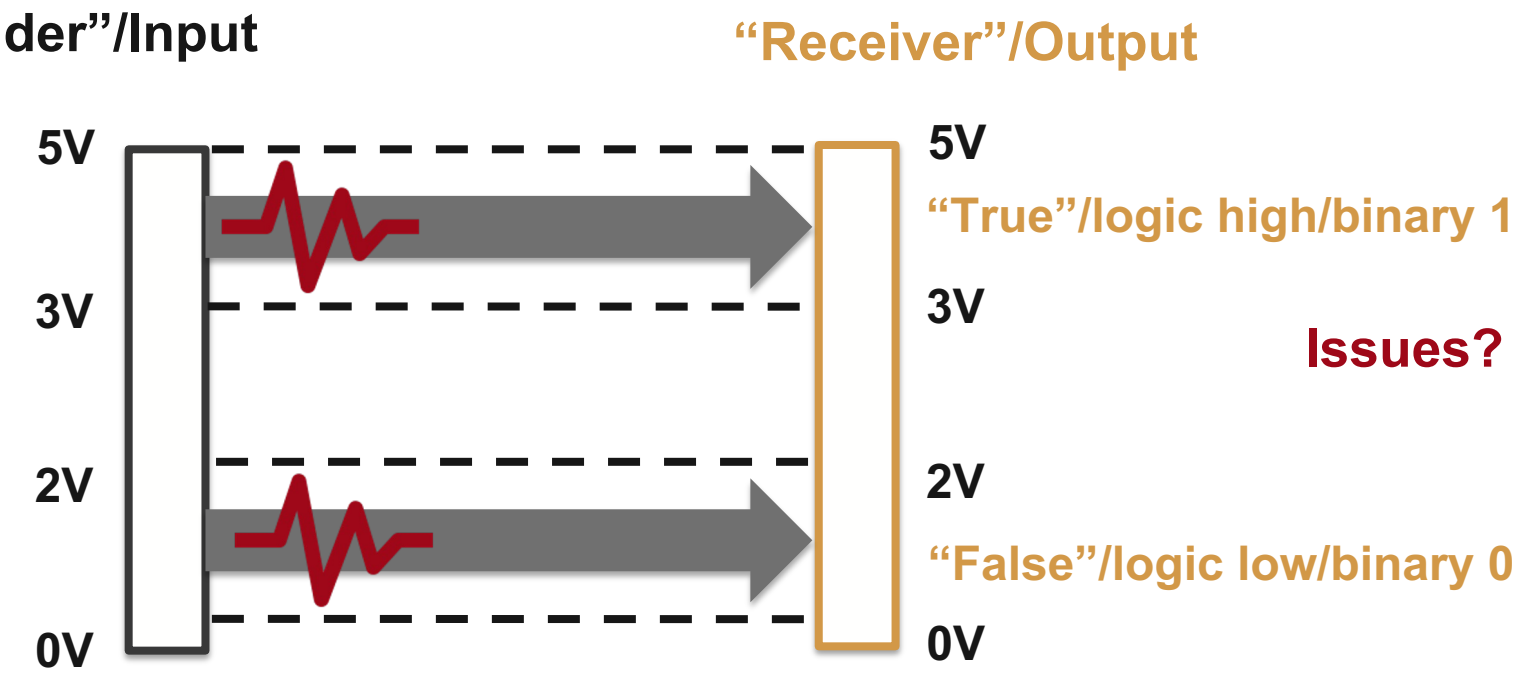
Why go digital? – the EE version

The classical demo in telecommunications: the very foundation of the digital abstraction



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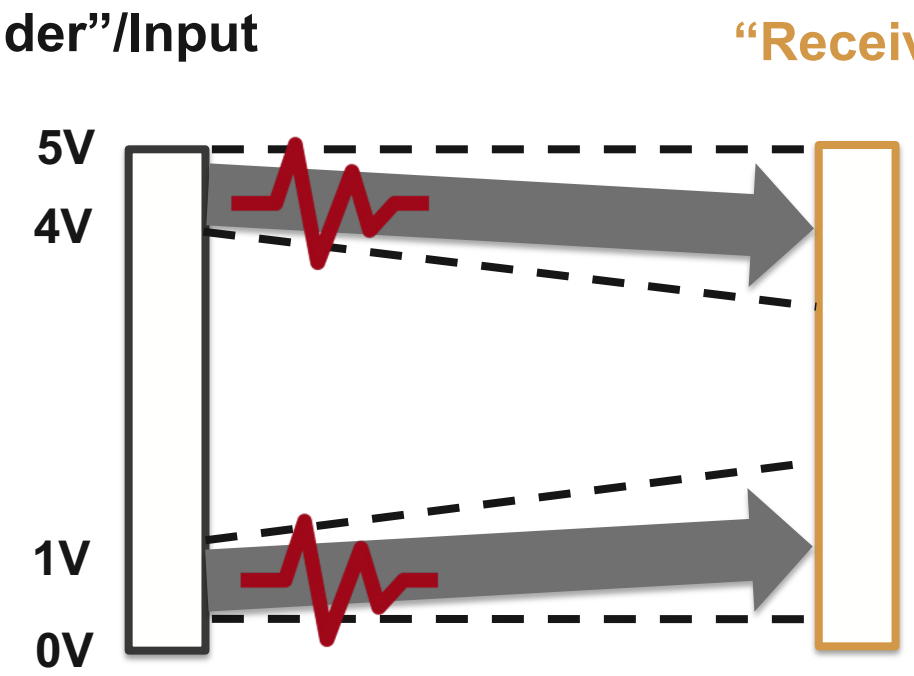
Why go digital? – the EE version

The classical demo in telecommunications: the very foundation of the digital abstraction



Now we have some tolerance to error/noise margin.

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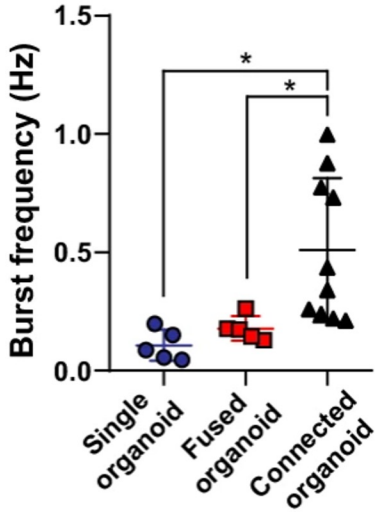
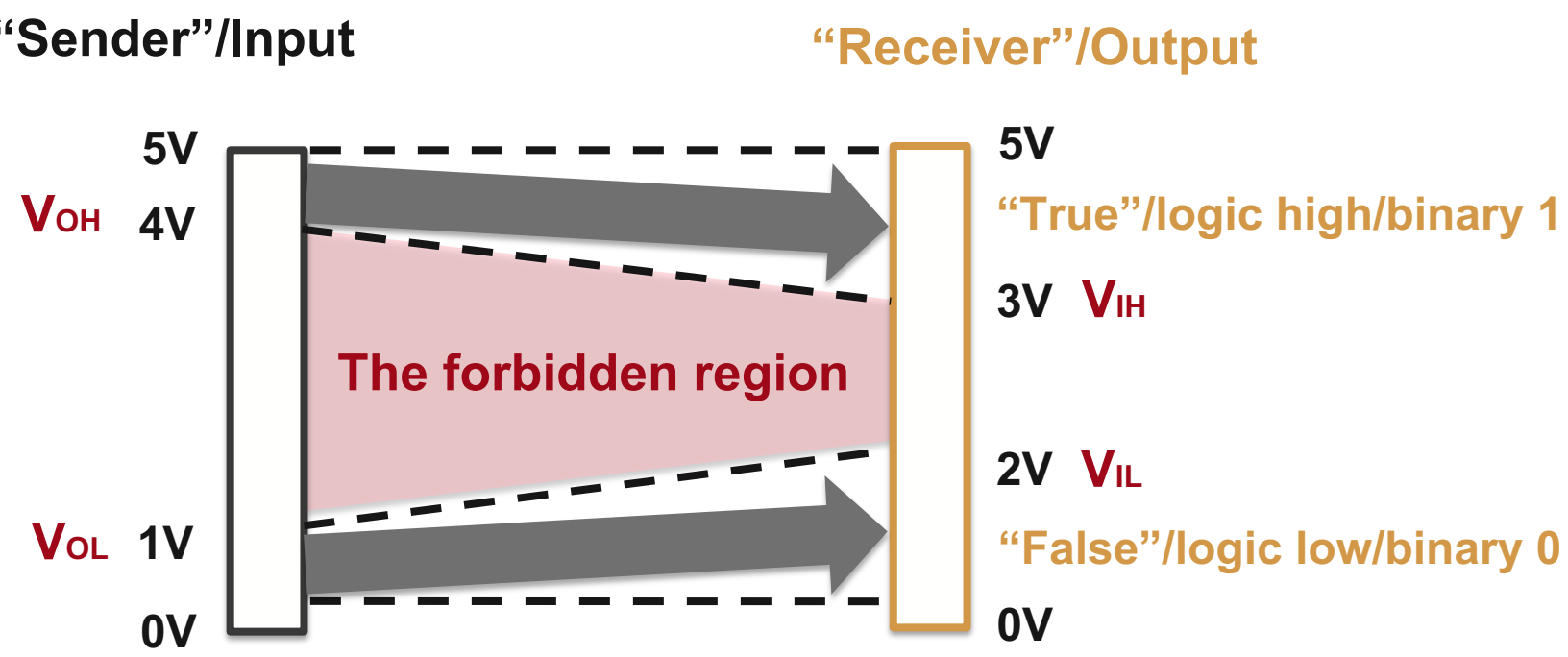
Why go digital? – the EE version

The very fundamental of the digital abstraction: the static discipline $V_{OL} < V_{IL} < V_{IH} < V_{OH}$

A good spreading between signals/data is how engineering works!



https://doi.org/10.1038/s41467-024-46787-7



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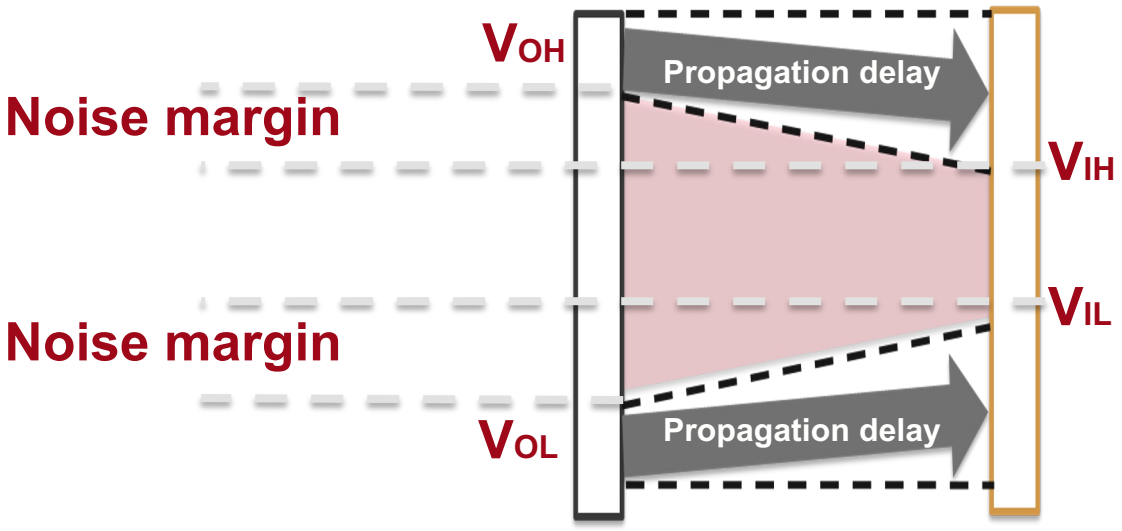
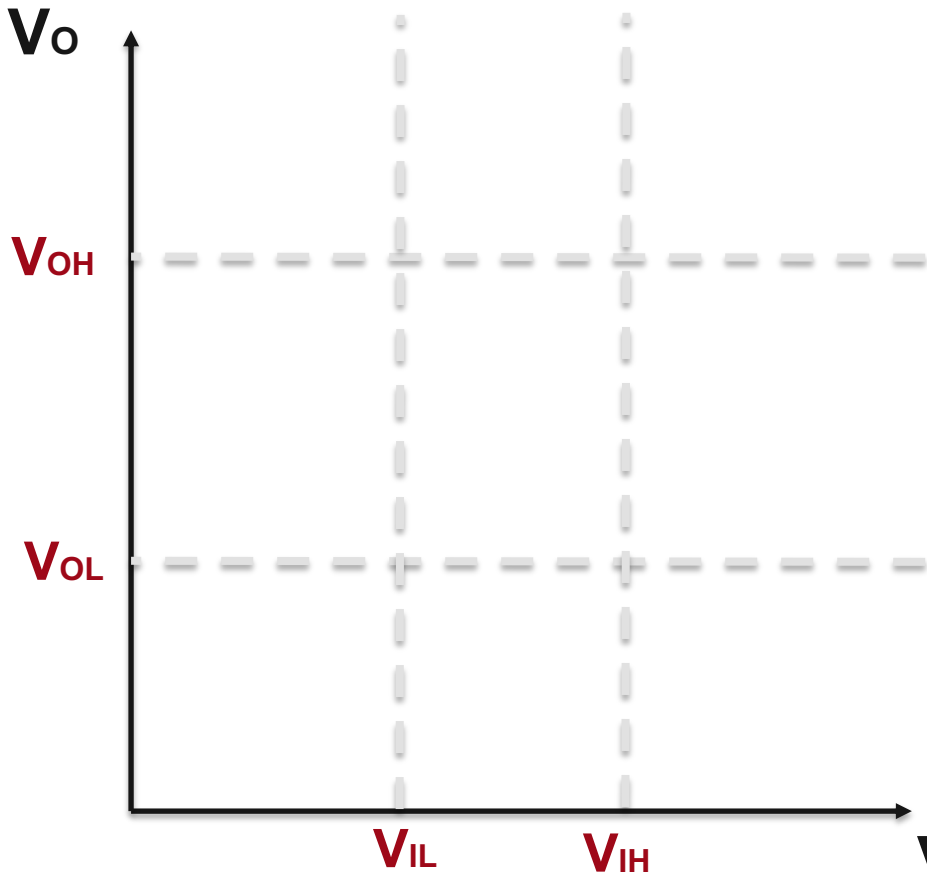
Technicality with this digitizing (quantizing)

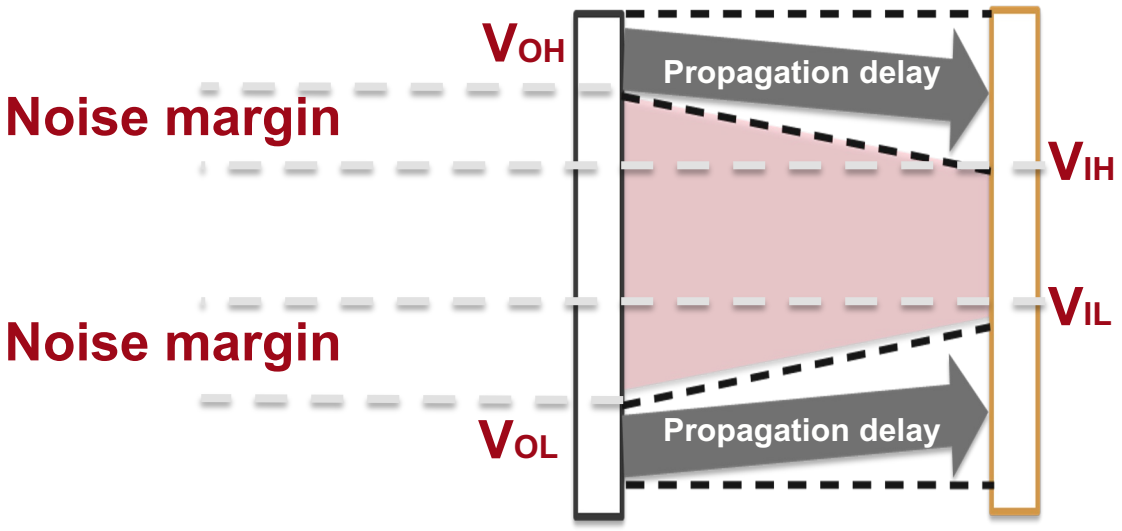


Voltage transfer characteristics

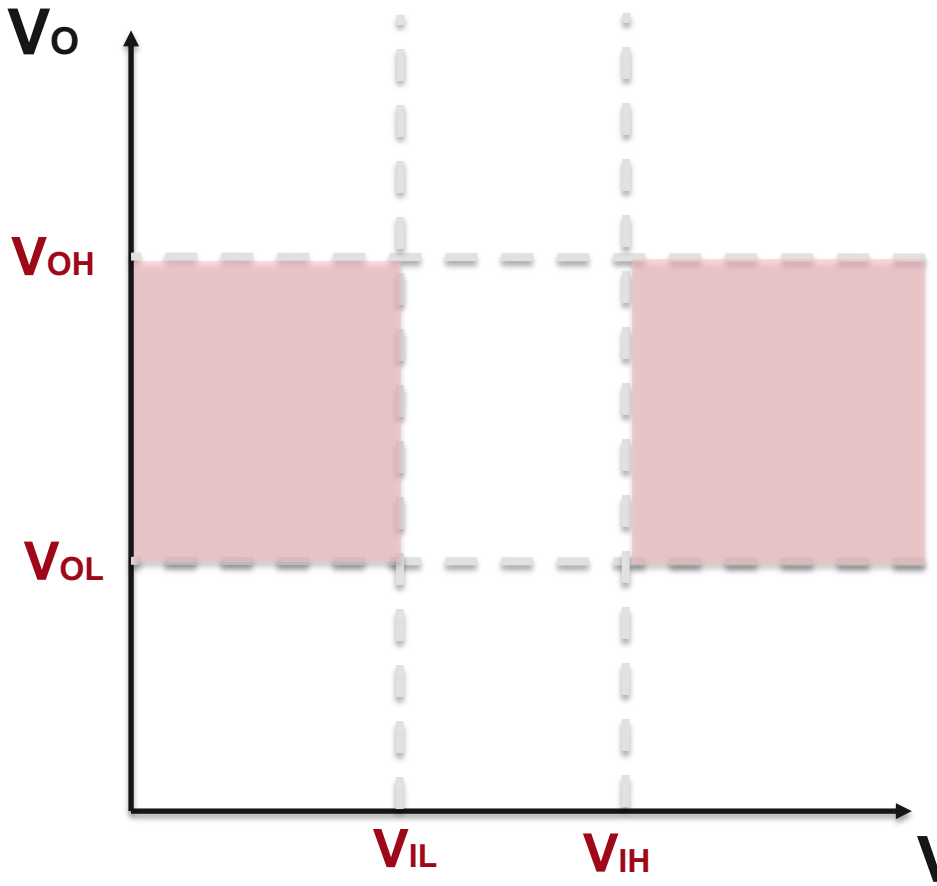


COE COLLEGE

Technicality with this digitizing (quantizing)



Voltage transfer characteristics



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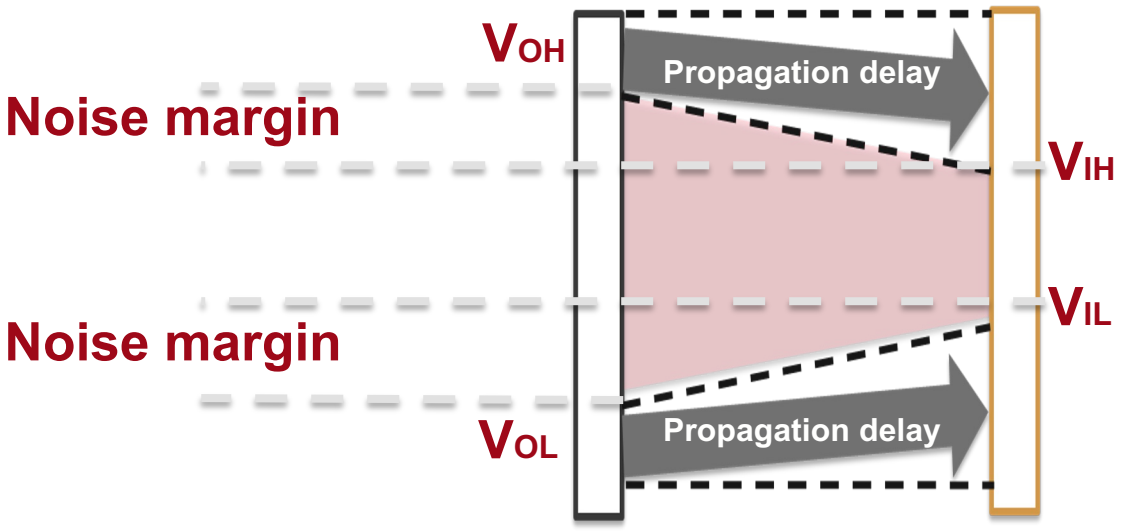
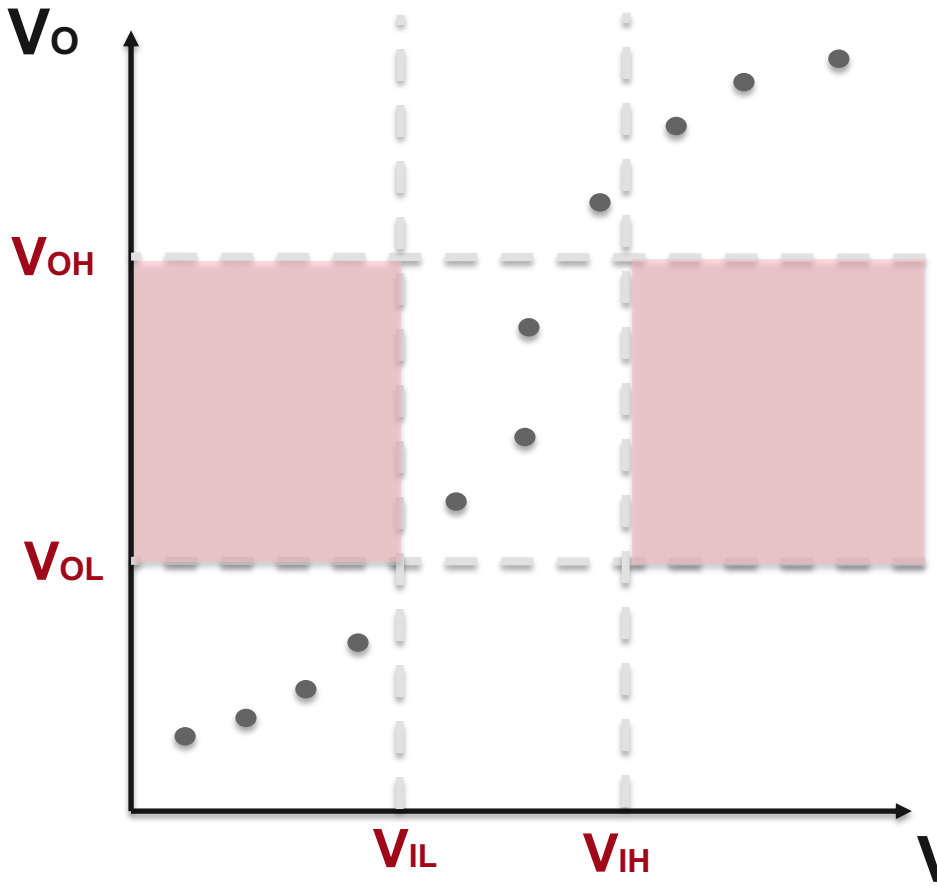
Technicality with this digitizing (quantizing)



Voltage transfer characteristics (VTC)



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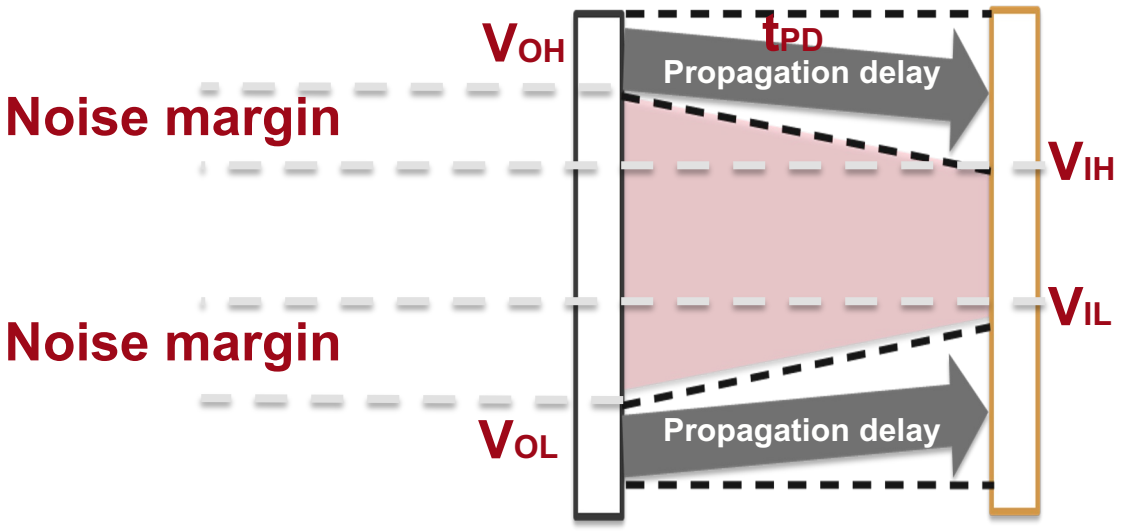
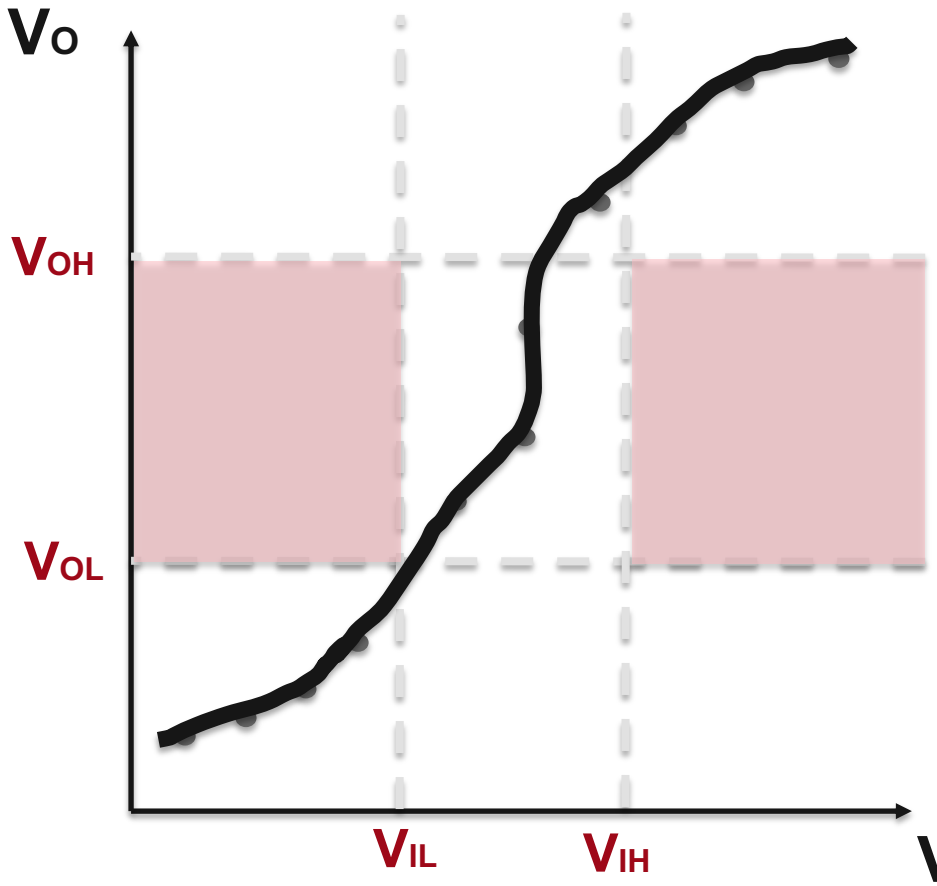
Technicality with this digitizing (quantizing)



Voltage transfer characteristics (VTC)



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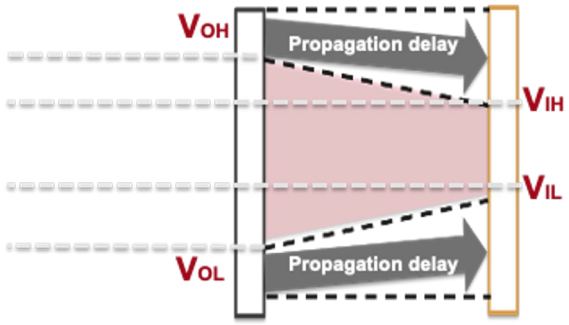
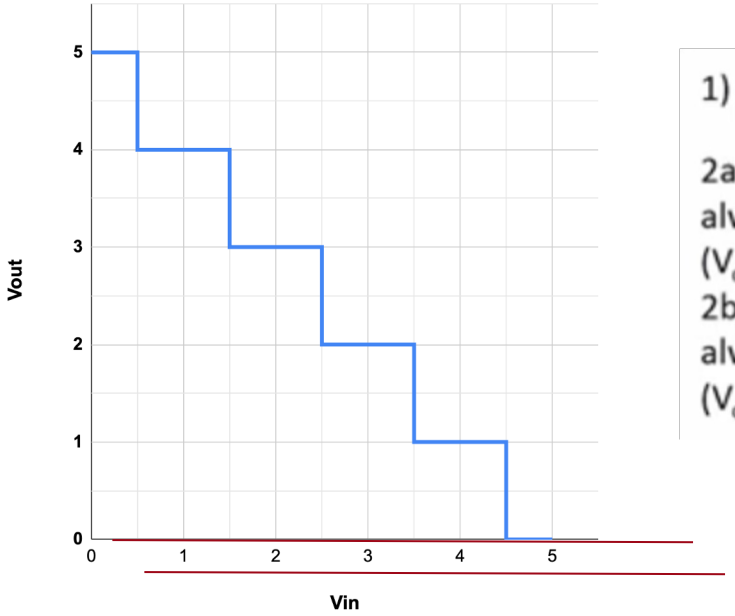
Not all I/O can satisfy the static discipline, and it won't be linear.

Static discipline example

$V_{OH} > V_{IH}$

$V_{OL} < V_{IL}$





1) $V_{ol} < V_{il} <= V_{ih} < V_{oh}$

2a) Does a valid low input ($V_{in} < V_{il}$) always produce a valid high output ($V_{out} > V_{oh}$)?

2b) Does a valid high input ($V_{in} > V_{ih}$) always produce a valid low output ($V_{out} < V_{ol}$)?

	Vol	Vil	Vih	Voh	Obeys Static Discipline
Specification #1	0.1	0.4	4.6	4.9	Yes No
Specification #2	0.6	0.9	4.1	4.4	Yes No
Specification #3	1.1	1.4	3.6	3.9	Yes No

If you follow the static discipline, a valid input will always produce a valid output.

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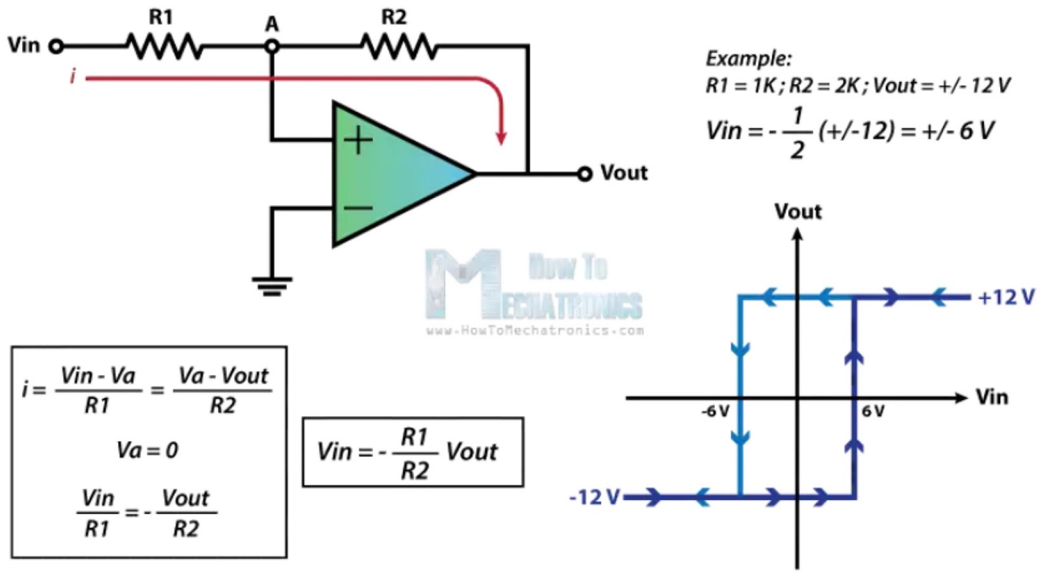
Practical digitizing (quantizing)



Non-Symmetrical Schmitt Trigger

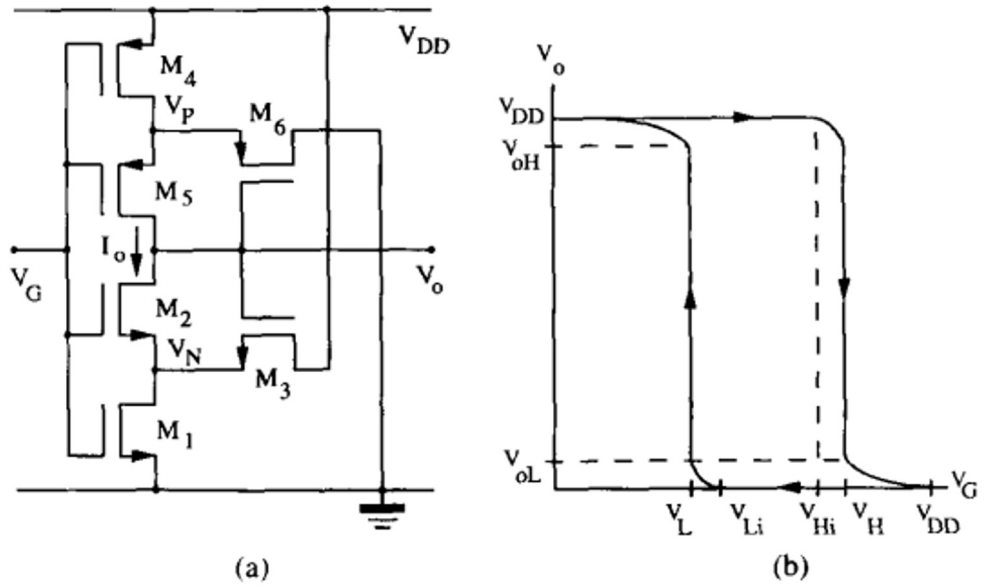


Fig. 1. CMOS Schmitt trigger and its transfer characteristic.

Filanovsky, I. M., and H. Baltes. "CMOS Schmitt trigger design." *IEEE Transactions on Circuits and Systems I: Fundamental Theory and Applications* 41.1 (1994): 46-49.

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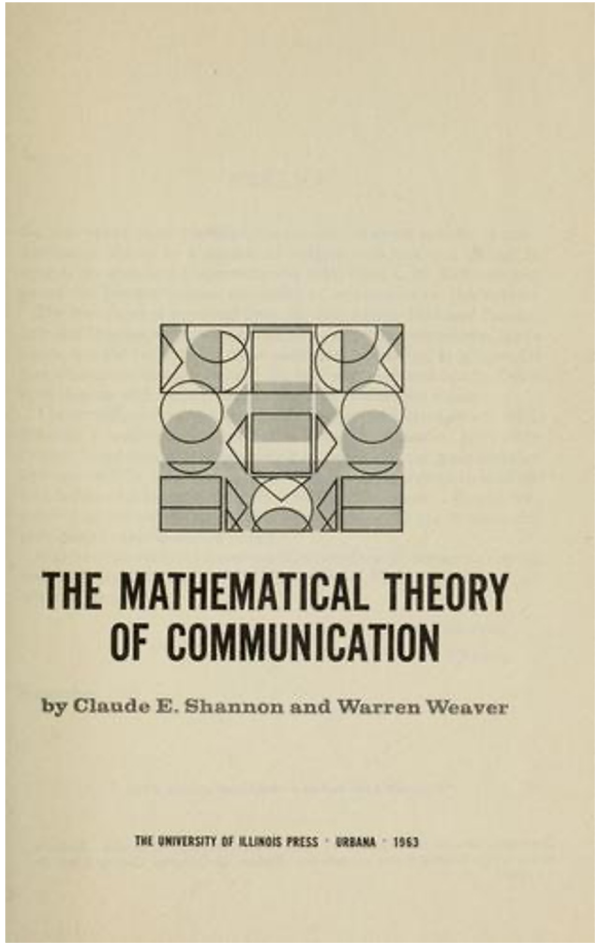
Why go digital? – the EE version

So now we can have stable 1s and 0s (in theory), so what?



It doubtless seems queer, when one first meets it, that information is defined as the *logarithm* of the number of choices. But in the unfolding of the theory, it becomes more and more obvious that logarithmic measures are in fact the natural ones.

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